

**REMARKS/ARGUMENT**

Applicants appreciate the time extended by the Examiner to discuss a few final issues with Applicant and his representative on September 23.

Applicants appreciate the Examiner's agreement with Applicants that all of the amendments to the Drawings proposed by Applicants on page 8 of the Amendment submitted on August 31, 2005 were supported by the specification save two typographical errors: (1) identification of paragraph [0056] on page 8, line 8 of the Amendment submitted on August 31, 2005 should instead be --[0064]--; and (2) "Phase Detection Circuit 60" should more correctly be changed to "an input of Figure 14".

Applicants agreed to the Examiner's request to add the element "responsive to a mode switch signal" to Claims 1 and 9, not in order to overcome any prior art, but in order to make the claim more accurate. The Examiner agreed with Applicants that a "mode switch signal" could be any of: CTL\_MODE\_P; CTL\_MODE\_A; OP\_ZPR; OA\_ZPR; CTL\_SRST; or some similar signal". Claims 17 and 18 have been canceled to avoid any duplication.

Applicants appreciate the Examiner's determination that the application is now in condition for allowance. Claims 1-16 stand allowable over the references of record. Applicants respectfully request allowance of the application as the earliest possible date.

Respectfully submitted,



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